

HE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Koichiro Tanaka

Art Unit: 2822

Serial No.: 09/842,797

Examiner: Maria F. Guerrero

Filed Title

: April 27, 2001

: METHOD OF FABRICATING SEMICONDUCTOR DEVICE

ENTERED W/RCE 10.20.04

MAIL STOP AF

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

AMENDMENT IN REPLY TO ACTION OF MAY 21, 2004

Please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 18 of this paper.

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